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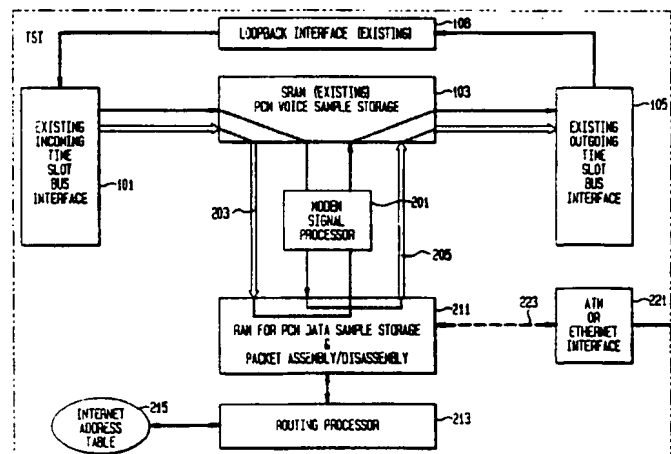
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(54) Switching internet traffic through digital switches having a time slot interchange network

(57) Apparatus and a method for interfacing between the Internet and the Telephone Network. A time slot interchange (TSI) is enhanced by the addition of a supplementary memory (211) for storing data for accumulating Internet packets. When a packet has been accumulated, the appropriate header is inserted into the packet under the control of a routing processor (213), and the packet can then be sent as a group of adjacent PCM samples over a connection to the Internet. In other embodiments, information is sent to the Internet over a direct data pipe for transmitting ATM cells or Ethernet

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FIG. 2



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Description**Technical Field:**

[0001] This invention relates to the switching of connectionless data traffic through digital switches of the type used for switching voice traffic.

Problem:

[0002] In recent years, the volume of Internet traffic has grown tremendously. Much of this traffic originates, is switched through, or terminates in a telephone switching system having a digital time slot interchange switching network. These switches which are now pervasive through the modern public switched telephone network were designed initially to handle voice and circuit switched data traffic. Subsequently, many of these switches were adapted to handle integrated switched digital network (ISDN) traffic which combines switched voice, circuit switched data traffic, and some (D-channel) data switched data traffic. At this point, these switches are being adapted to handle data traffic required for serving Internet customers, traffic which when it enters the Internet backbone network, becomes connectionless.

[0003] The arrangements available in the prior art for serving connectionless traffic in telephone type switching systems are not efficient. One such arrangement uses an adjunct processor connected from and to a time slot interchange (TSI) network via trunk circuits of the type used for communicating with distant switching systems. This arrangement is expensive in its use of time slots and requires a large amount of memory in addition to the memory provided within the TSI.

[0004] In the prior art, connections through a telephone access switch are made directly to the Internet backbone network; this ties up a trunk to the Internet network for the duration of an Internet call.

[0005] Another alternative is to use the type of packet switch unit added to digital switches in order to process the relatively small amount of data switched traffic of ISDN lines. Such units have inadequate capacity, and also require the use of extra time slots and extra memory in addition to the TSI memory. A first problem with the prior art, therefore, is that there is no efficient way of handling the connectionless data traffic of Internet type data traffic.

Solution:

[0006] The above problem is solved, and an advance is made over the prior art in accordance with our invention wherein a processor means is directly tied to the memory of a TSI. This processor accumulates and transmits packets in a packet assembler-disassembler, and inserts output packets into an output buffer. In one embodiment of the Applicants' invention, the processor

inserts output packets into a loopback buffer (normally used to write diagnostic patterns into the TSI memory); this processor, advantageously can access all time slots of the TSI since this is a fundamental characteristic of the TSI. The processor provides a centralized location for rate adaptation to allow slow rate Internet traffic to be adapted to fast rate transmission. The operations, administration, and maintenance (OA&M) arrangements for the processor can be readily combined with the OA&M arrangements of the switch. The higher reliability of a telephone switch can be used advantageously to provide similar high reliability for this Internet type traffic. The processor can readily access the subscriber data base, translations, and the subscriber line signaling information received in the switch.

[0007] In accordance with one preferred embodiment of the Applicants' invention, a digital signal processor in the TSI is used to modulate/demodulate PCM signals generated using analog lines and modems prior to being accumulated in the packet assembler-disassembler. Advantageously, this eliminates the need for external modems, that require extra time slots, to condition the PCM signals. In accordance with Applicant's preferred embodiment, the routing processor has access to all egress links via the loopback buffer access to all memories of the TSI. Advantageously, the processor may be connected to any idle time slot.

[0008] In a second embodiment of the Applicants' invention, the processor may be directly connected to data interfaces such as an ATM data interface that can be readily added to the TSI. In this embodiment, the Internet traffic is transmitted over a data network as a plurality of ATM cells.

[0009] In accordance with a third embodiment of the Applicants' invention, the processor may be directly connected to an LAN Interface such as Ethernet that could be readily added to the TSI. In this embodiment, the Internet traffic is transmitted directly to resident server advantageously providing Intranet capabilities for subscribers.

Brief Description of the Drawing:**[0010]**

Fig. 1 is a diagram of a prior art time slot interchange and interface circuits;

Figs. 2 and 3 are block diagrams of preferred embodiments of Applicants' invention for interfacing between the telephone plant and the Internet or other data network; and

Fig. 4 is a block diagram illustrating the steps for establishing connections between two PC's (Personal Computers) via the system shown in Figs. 2 and 3.

Detailed Description:

[0011] Figure 1 is from the prior art, and shows in simplified form how a time slot interchange works to switch, for example, telephone traffic. An existing time slot bus interface 101 interfaces with various line unit and trunk unit circuits which provide multiplexed pulse code modulation (PCM) input signals. The signals are placed in input line time slots 111 and input trunk time slots 113, and are stored in a static random access memory (SRAM) 103. The contents of the SRAM are sent to outgoing line time slots 121, and outgoing trunk time slots 123 to an existing outgoing time slot bus interface 105 for interfacing with the outgoing side of line and trunk units. It is to be understood that any time slot can be used for a line signal or for a trunk signal in terms of the basic operation of the TSI.

[0012] Figure 2 shows the enhancements of Figure 1 necessary to implement Applicants' invention. The trunk time slots are sent as signals 203 directly to a RAM 211 for storing PCM signals and assembling and disassembling packets. Line time slots are sent to a modem signal processor 201 which converts analog PCM data to pure digital data for use by unit 211. The modem signal processor 201 also converts pure digital data from unit 211 into analog PCM type data for transmission to line time slots 121. The assembled or disassembled packet in RAM 211 is modified by routing processor 213 to have the correct data address. The routing processor 213 then loads the packet into the TSI outgoing interface 105. In performing its routing, the routing processor consults an Internet address table 215. The Internet address table is populated and entries are cleared from that table in accordance with the well-known teaching of the prior art.

[0013] In the second embodiment, the packets are loaded or unloaded from an ATM Interface 221 over a packet data pipe 223 instead of the trunk time slots. In the third embodiment, that can provide Intranet services, the packets are loaded or are unloaded from an Ethernet interface 221 instead of the trunk timeslots. Just as any time slot in the existing TSI can communicate with any other time slot, in this embodiment, any packet, ATM cell or time slot can communicate with any other packet, ATM cell or time slot. The ATM interface can be used for interfacing directly with an ATM network. The Ethernet interface can be used for communicating with a data server (e.g., data base, Gateway router, proxy server, or traffic measurements analyzer), and can be used for interfacing with another routing processor that is co-located in the same switch.

[0014] In order to send a data stream to a selected time slot of an outgoing time slot interface 105 of a different slice of the TSI unit, the data stream is sent from interface 105 to the loopback interface 108 and thence to the incoming time slot bus interface 101. Inputs to interface 101 are automatically broadcast to all the SRAM units of the other slices of the TSI unit, from which

the data stream can be sent to the selected time slot. In the prior art, the loopback interface 108 is only used as a diagnostic tool. Advantageously, this provides an inexpensive arrangement for accessing outgoing time slots of slices not equipped with a packet assembly/disassembly unit and routing processor, from a slice that is so equipped.

[0015] Figure 3 illustrates routing voice traffic over an Internet network. The modem signal processor 201 is replaced by a vocoder signal processor 301 so that PCM voice can be converted into packetized voice which is a much more efficient way of carrying voice signals. The routing processor now processes the H.323 and TCP/IP protocol information; this processing encompasses call setup and teardown, datagram routing, and call control. The routing that is required is telephony routing rather than Internet routing, and this is accomplished by having the protocol processor 313 communicate with a switching module processor 315 for the purposes of digit analysis and routing of voice signals; the SMP 315 determines the destination address of the deactivation vocoder, and informs the routing processor of this address. The routing processor ensures that datagrams leaving that vocoder will be routed to the appropriate destination.

[0016] Figure 4 is a diagram illustrating the process of setting up a call. Rectangles are system processes i.e., processes which are active as long as the switch is active, whereas circles are terminal processes which are created and live only for the duration of the call. The process is started when a peripheral control process 801 in Switching Module Processor (SMP) 1 (851), the SMP for serving the originating line, detects an off-hook signal from the caller. It sends a message 803, to create an originating terminal process 811. It subsequently sends additional messages 805 representing digits dialed or Internet addresses keyed by the caller. These are transmitted in messages 805. The originating terminal process 811 then requests a routing and terminal allocation - local routing system process 813 to route the call. Process 813 sends a line terminating request 814 to a routing and terminal allocation - terminating system process 815 in SMP-N 853, the SMP which serves the called customer.

[0017] Process 815 then sends a message 817 to create a terminating terminal process 821, and sends to that process an identification of the terminating line 819. The terminating terminal process 821 instructs (via a function call) the peripheral control 825 to alert the terminating terminal. Terminating terminal process 821 sends a message 823 to originating terminal process 811 after peripheral control 825 completes the connection to indicate that the set-up has been completed. The terminating terminal automatically answers the call, and peripheral control 825 detects this answer and sends a message 827 to the terminating terminal process 821 verifying this answer. In response to receipt of the off-hook message from peripheral control 825, terminating

terminal process 821 sends an answer message 829 to the originating terminal process.

[0018] All of the above call processing is generally performed, as in the prior art, as implemented for example on Lucent Technologies Inc.'s, 5 ESS® switch. The newly added elements Blocks 833 and 835 reside in the packet processor complex 211 of Applicants' invention. For Internet data or telephony calls, RTA terminating system process 815 sends a start task message 831 to a data routing system process 833 of the packet processor complex. This process interfaces with packet processing software to control the actions of the peripheral control process 835. The packet processor hardware complex processes any information in a modem buffer for interfacing with a modem signal processor 201 (Fig. 2), updates routing data, initiates packet billing measurement task to control the total number of packets in a call. After the packet processor complex has completed its setup task, it sends a task complete message 837 to the routing and terminal allocation terminating system process 815 to cut through the call paths. The packet processor complex then processes packets, as described earlier, for the duration of the call. The call is torn down when the line or trunk hangs up (disconnects). The per call resources (OTP 811, TTP 821 and PPC 835 tasks) are released and can then be reallocated to another call.

[0019] The above description is of one preferred embodiment. Many other embodiments will be apparent to those of ordinary skill in the art, without departing from the scope of the invention. The invention is only limited by the attached Claims.

Claims

1. Apparatus for performing a digital switching function comprising:

an incoming time slot bus interface;

a memory for storing PCM samples from said incoming time slot bus interface; and

an outgoing time slot bus interface for receiving samples stored in said memory;

characterized in that:

said apparatus further comprises an additional memory for storing incoming PCM samples for performing a packet assembly-disassembly function and for transmitting output data samples to said memory; and

a routing processor for controlling said second memory and packet assembler-disassembler wherein packetized traffic received from said

incoming time slot bus interface via said memory are assembled into packets provided with a proper header by said routing processor for transmission via said memory to said outgoing time slot bus interface.

2. The apparatus of Claim 1 further comprising:

a modem signal processor for converting between analog digital signals and binary signals received from and transmitted to said memory;

said modem signal processor connected to said another memory for storing said binary signals into said memory.

3. The apparatus of Claims 1 or 2 wherein said another memory is connected via a data packet pipe to a data network.

4. The apparatus of Claim 3 wherein said data network is an asynchronous transfer mode (ATM) network.

5. The apparatus of Claim 3 wherein said another memory is connected via a data packet pipe to an Ethernet interface for communicating with another server co-located with said apparatus.

6. The apparatus of Claims 1 or 2 further comprising an Internet address table wherein said routing processor uses information from said Internet address table to formulate packet headers for packets assembled in said another memory.

7. The apparatus of Claim 1 further comprising a Vocoder signal processor for accepting PCM voice samples from said memory for storage as vocoded voice signals in said another memory wherein ones of voice channels received from said incoming time slot interface are transmitted as vocoded data samples to said outgoing time slot bus interface.

8. The apparatus of Claim 2 wherein said modem signal processor is further utilized for rate conversion, by storing input binary signals at a different rate than the rate of converting said stored signals into modem outputs.

9. The apparatus of Claims 1 or 2, wherein said incoming interface, memory for storing PCM samples and outgoing interface are on one slice of a time slot interchange (TSI) unit, further comprising:
 - a loopback interface for broadcasting selected outgoing time slots of active communications to memories of other slices of said TSI unit.

10. A method of establishing an Internet connection comprising the steps of:

responsive to receipt of telephone number digits representing an Internet call establishing an originating terminal process;

said original terminating process establishing a RTA (routing and terminal allocation) of LRSP, (local routing system process);

said RTA/LRSP sending an internal system message to create an RTA terminal system process;

said RTA terminal system process initiating a data routing system process in a packet processor complex;

said RTA terminal system process creating a terminating terminal process at another half call end of said connection;

said terminating PC transmitting an off-hook message to said terminating terminal process;

said terminating terminal process transmitting a message confirming establishment of the connection to said originating terminal process.

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FIG. 1
(PRIOR ART)

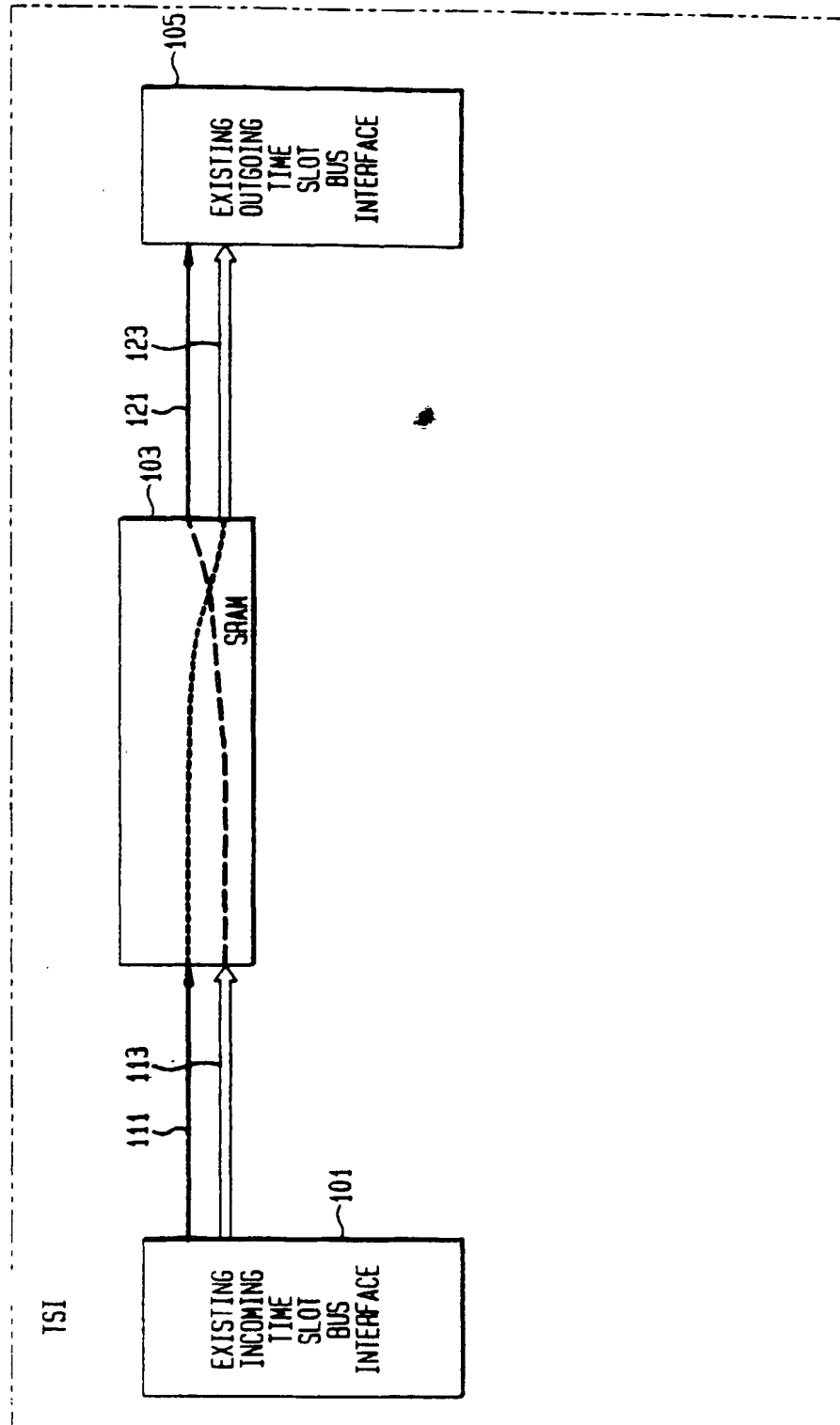


FIG. 2

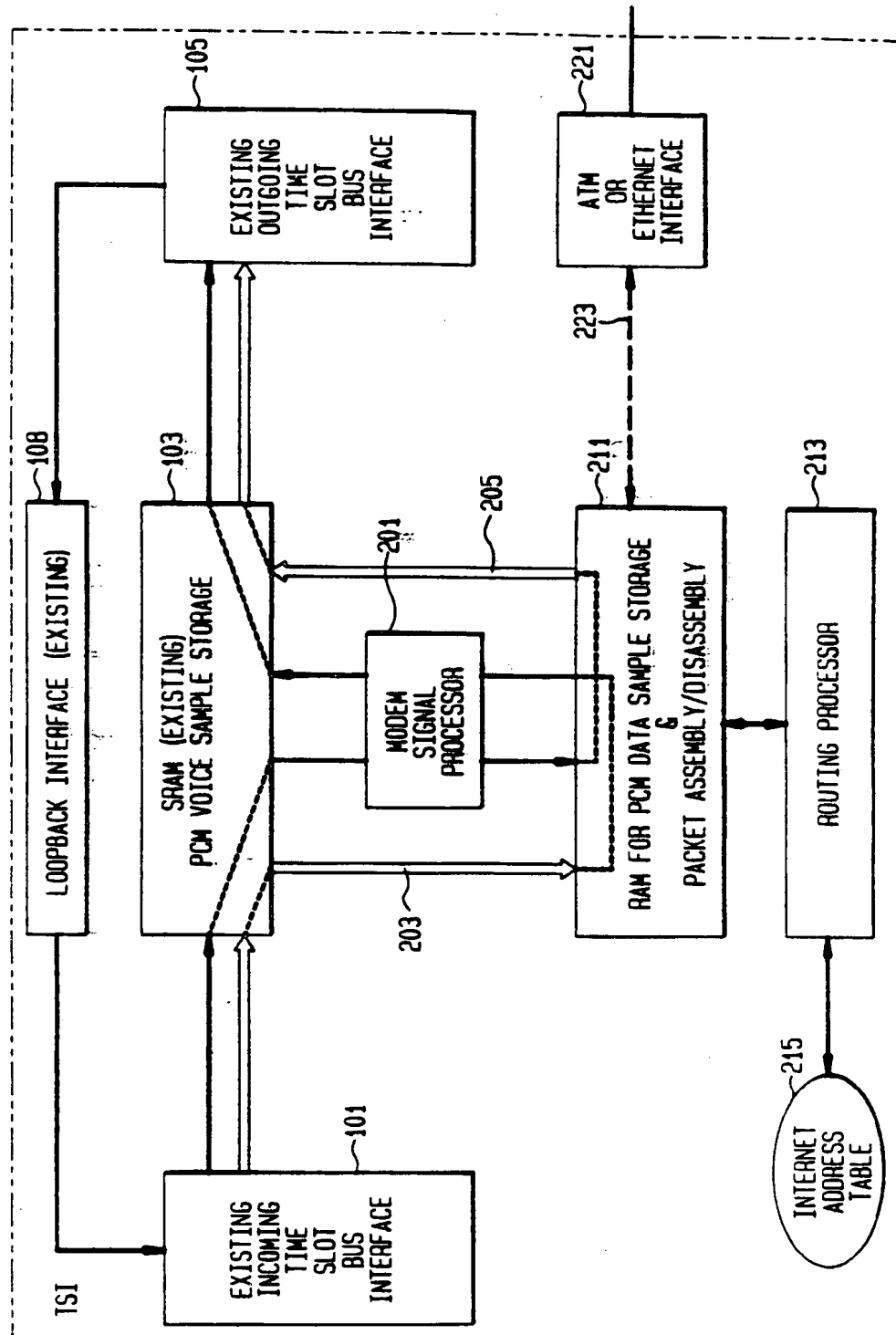


FIG. 3

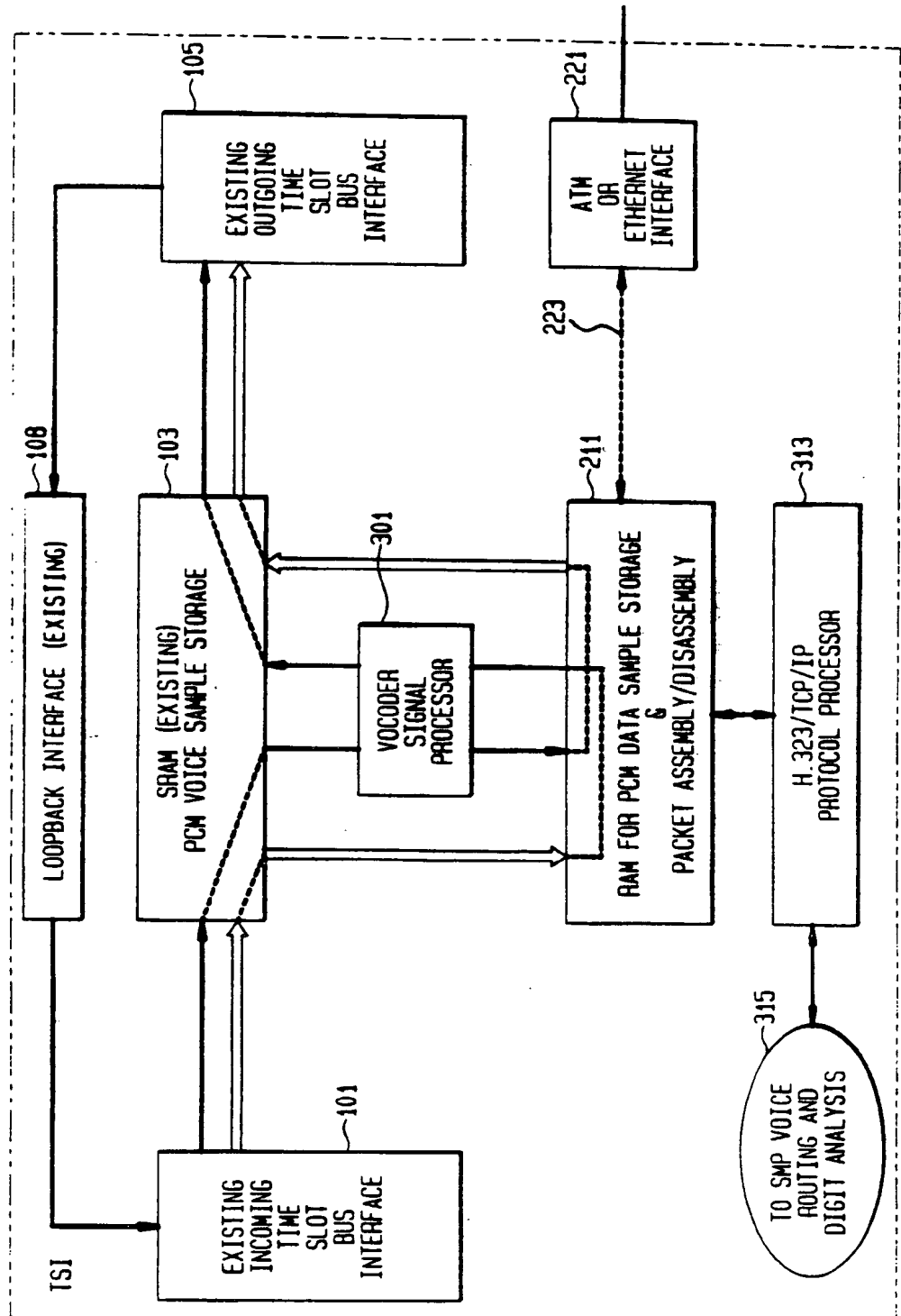
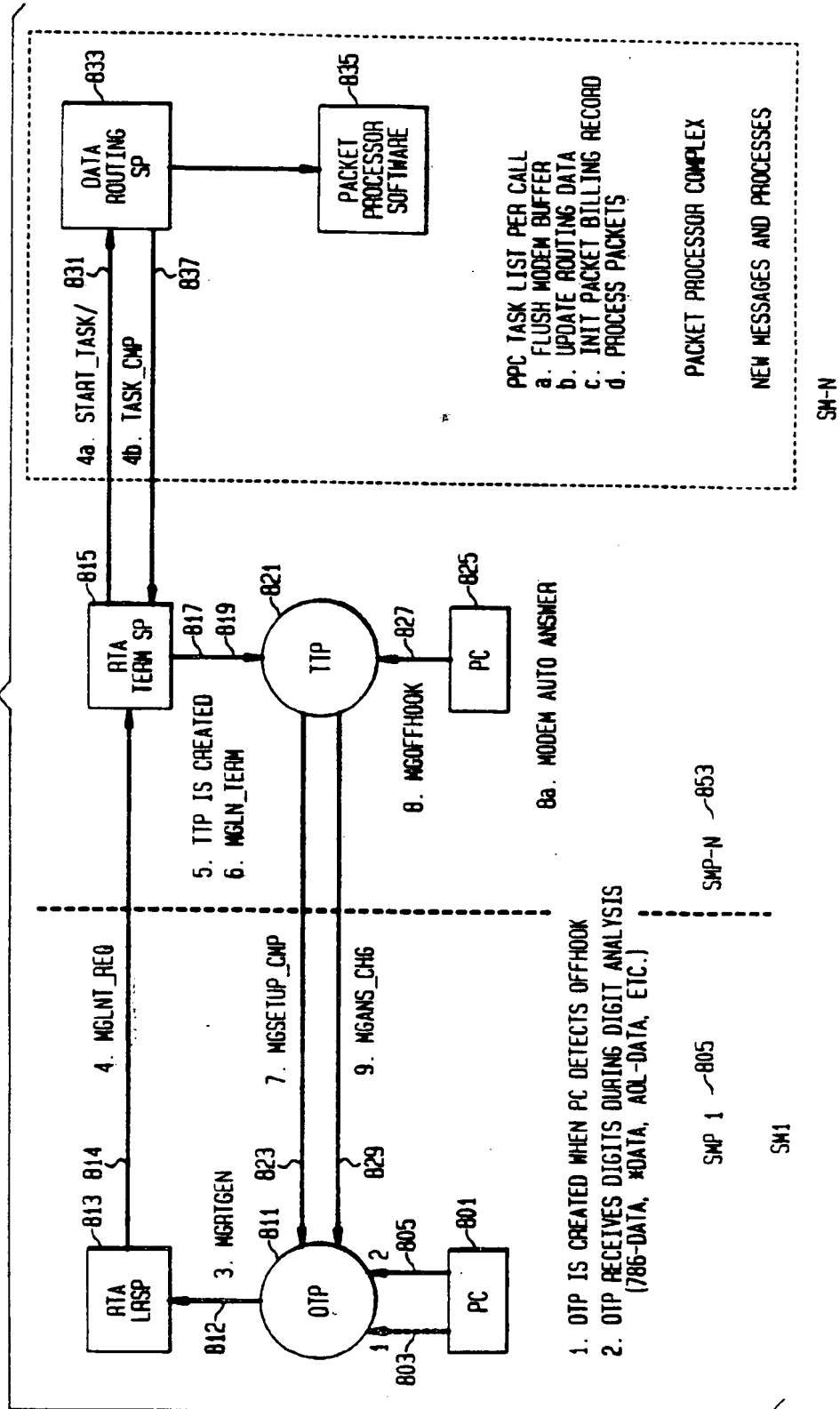


FIG. 4



under Rule 48, paragraph 1 of the European Patent Convention EP 99 38 4075

EP 99 38 4075

FPO FORM 1503 03 02 (P04C27)

**ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.**

EP 99 30 4075

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on
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27-08-1999

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For more details about this annex : see Official Journal of the European Patent Office, No. 12/82



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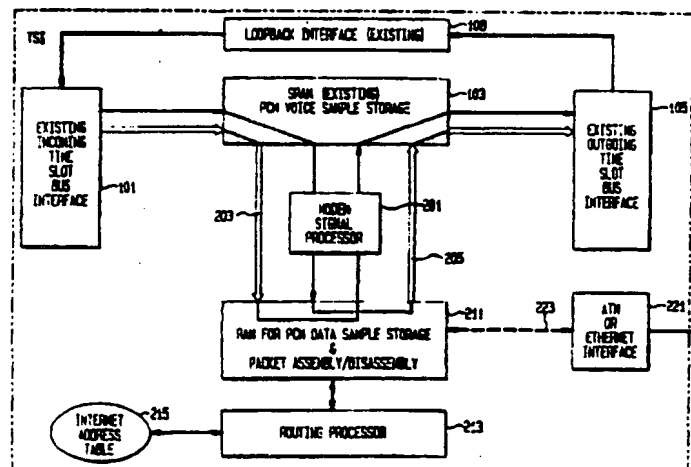
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FIG. 2





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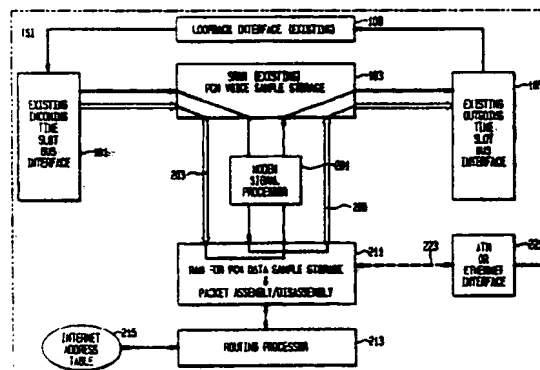
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FIG. 2



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EUROPEAN SEARCH REPORT

Application Number
EP 99 30 4075

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The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 6 March 2000	Examiner Gijssels, W
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X: particularly relevant if taken alone Y: particularly relevant if combined with another document of the same category A: technological background O: non-written disclosure P: intermediate document</p> <p>T: theory or principle underlying the invention E: earlier patent document, but published on, or after the filing date D: document cited in the application L: document cited for other reasons A: member of the same patent family, corresponding document</p>			

EPO FORM 1503 (Rev. 01/97)



European Patent
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Application Number

EP 99 30 4075

CLAIMS INCURRING FEES

The present European patent application comprised at the time of filing more than ten claims.

- ☐ Only part of the claims have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims and for those claims for which claims fees have been paid, namely claim(s) 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52, 53, 54, 55, 56, 57, 58, 59, 60, 61, 62, 63, 64, 65, 66, 67, 68, 69, 70, 71, 72, 73, 74, 75, 76, 77, 78, 79, 80, 81, 82, 83, 84, 85, 86, 87, 88, 89, 90, 91, 92, 93, 94, 95, 96, 97, 98, 99, 100, 101, 102, 103, 104, 105, 106, 107, 108, 109, 110, 111, 112, 113, 114, 115, 116, 117, 118, 119, 120, 121, 122, 123, 124, 125, 126, 127, 128, 129, 130, 131, 132, 133, 134, 135, 136, 137, 138, 139, 140, 141, 142, 143, 144, 145, 146, 147, 148, 149, 150, 151, 152, 153, 154, 155, 156, 157, 158, 159, 160, 161, 162, 163, 164, 165, 166, 167, 168, 169, 170, 171, 172, 173, 174, 175, 176, 177, 178, 179, 180, 181, 182, 183, 184, 185, 186, 187, 188, 189, 190, 191, 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European Patent
Office

LACK OF UNITY OF INVENTION
SHEET B

Application Number
EP 99 30 4075

The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:

1. Claims: 1-9

Apparatus for a digital switching function in which additional memory and a routing processor are used for storing PCM samples, performing packet assembly-disassembly function, provide packets with proper header, and transmit to outgoing time slot bus interface.

2. Claim : 10

Method of establishing an internet connection by receiving telephone number digits, establishing a RTA of LRSP, sending an internal system message, initiating a data routing system process, creating a terminal terminating process, and transmitting off-hook and connection establishment messages.

**ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.**

EP 99 30 4075

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on
The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

06-03-2000

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For more details about this annex : see Official Journal of the European Patent Office, No. 12/82